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PTO/SB/08A (08-00)

Substitute for form 1449A/PTO		<i>Complete if Known</i>
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number 09/844,673
		Filing Date April 30, 2001
		First Named Inventor THEKKATH
		Group Art Unit 2192
		Examiner Name Rutten, James D.
Sheet	1 of 1	Attorney Docket Number
		MTEC-006/00US

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	1/31/06
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OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JDR	D1.	F. Chow et al., "Engineering a RISC Compiler System," <i>IEEE COMP-CON</i> , March 1986, pp. 132-137.	
JDR	D2.	M.D. Smith, "Tracing with Pixie," <i>Technical Report CSL-TR-91-497</i> , Stanford University, Computer Systems Laboratory, November 1991, pp. 1-29.	
JDR	D3.	ATOM Reference Manual, <i>Digital Equipment Corporation</i> , Massachusetts, December 1993, pp. 1-32.	
JDR	D4.	A. Srivastava et al., "ATOM: A System for Building Customized Program Analysis Tools," <i>WRL, Research Report 94/2</i> , Digital Equipment Corporation, Massachusetts, March 1994, pp. 1-23.	
JDR	D5.	ATOM User Manual, Digital Equipment Corporation, March 1994, pp. 1-28.	
JDR	D6.	B. Cmelik et al., "Shade: A Fast Instruction-Set Simulator for Execution Profiling," <i>Proceedings of the 1994 ACM SIGMETRICS Conference</i> , SIGMETRICS, California, May 1994, pp. 128-137.	
JDR	D7.	MIPS Technologies; "MIPS64 5K™ Processor Core Integrator's Guide;" [Document No. MD00056]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.2; August 11, 2000; pp. I-II, 1-82.	
JDR	D8.	MIPS Technologies; "MIPS64™ 5K™ Processor Core Family Integrator's Guide;" [Document No. MD00106]; ©1999-2001 <i>MIPS Technologies, Inc.</i> ; Rev. 02.00; January 15, 2001; pp. I-VIII, 1-75.	
JDR	D9.	MIPS Technologies; "Core Processor Interface Specification;" [Document No. MD00068]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.11; March 30, 2001; pp. 1-26.	

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